



KPR INSTITUTE OF ENGINEERING AND TECHNOLOGY, Coimbatore (Autonomous)

CIAT II TIMETABLE FOR PG PROGRAMME (ACADEMIC YEAR 2025-2026 - SEMESTER 03)

Date & Session		CAD/CAM	CSE	STRUCTURAL	VLSI Design
06.11.2025	FN	NA	NA	NA	NA
	AN	P23CCP06 - Sensors and Instrumentation	P23CSP07 - Cyber Security Practices	P23ST301 - Advanced Prestressed Concrete	P23VLP14 - Industrial Internet of Things
07.11.2025	FN	NA	NA	NA	NA
	AN	P23CCP15 - Project management	P23CSP09 - Social Network Analysis	P23STP11 - Design of Sub Structures	P23VLP20 - Low Power VLSI Design
08.11.2025	FN	NA	NA	NA	NA
	AN	NA	NA	P23STP13 - Design of Steel Concrete Composite Structure	P23VLP08 - Testing of VLSI Circuits
Question Paper Pattern: Part A - 10*1=10 Marks Part B - 10*2=20 Marks Part C - 1*6=6 Marks & 2*12 = 24 Marks				Maximum Marks : 60 Marks Test Portion : 2.5 Units CIAT Time : FN - 09:10 am to 10:40 am AN - 02:30 pm to 04:00 pm	


 ✓ Controller of Examinations